

Electronic Acknowledgement Receipt

EFS ID:	2586684
Application Number:	10716146
International Application Number:	
Confirmation Number:	5288
Title of Invention:	Time delay compensation circuit comprising delay cells having various unit time delays
First Named Inventor/Applicant Name:	Geun-Hee Cho
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File Listing:					
Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Amendment - After Non-Final Rejection	8021-180-amd2.pdf	94712 3c51de900bda0b116c0a4f5143cf95fabb abde144	no	5
Warnings:					
Information:					
2	Fee Worksheet (PTO-06)	fee-info.pdf	8170 810005e6fe83852d29800de5b6fbab0 5221fa32	no	2
Warnings:					
Information:					
Total Files Size (in bytes):				102882	
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